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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE (USPTO)**

<b>Serial Number</b>	10/712,988
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<b>Filing Date</b>	November 12, 2003
<b>Title of Application</b>	METHOD OF GENERATING A CHAOS-BASED PSEUDO-RANDOM SEQUENCE AND A HARDWARE GENERATOR OF CHAOS-BASED PSEUDO RANDOM BIT SEQUENCES
<b>First Named Inventor</b>	Ljupco Kocarev
<b>Assignee</b>	STMicroelectronics S.r.l.
<b>Group Art Unit</b>	2193
<b>Examiner</b>	Ngo D. Chuong
<b>Attorney Docket Number</b>	2110-85-03
<b>Nature of the Office Communication to which this is responding</b>	Final Office Action
<b>Date of the Office Communication</b>	January 12, 2010
<b>Nature of this Document</b>	Supplemental Response to Final Office

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**CERTIFICATE OF MAILING OR TRANSMISSION**

I hereby certify that this correspondence is being transmitted via the Office electronic filing system, EFS-Web, addressed to: MS AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 9<sup>th</sup> day of April, 2010.

/Cindy McKee/

Signature